

Saswati Soumya Sahoo
Department - IT

BHUBANANANDA ODISHA SCHOOL OF ENGINEERING, CUTTACK
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
LESSON PLAN

Discipline:	Semester: 4 th	No. Of period available	Name of the Teaching Faculty:
Subject: MPMC	No. Of Days/per week class allotted: 5 periods per week Tue,Wed,Thu,Fri,Sat		Saswati Soumya Sahoo
Week	Class date		Topics to be covered
1 st	10.03.22	1	Unit 1 : Microprocessor (Architecture & Programming -8 bit 8085) 1.1 Introduction to Microprocessor and Microcomputer & distinguish Between them.
	11.03.22	1	1.2 Concept of Address bus, data bus, control bus & System Bus
	12.03.22	1	1.3 General Bus structure Block diagram.
2 nd	15.03.22	1	1.4 Basic Architecture of 8085 (8 bit) Microprocessor
	16.03.22	1	1.5 Signal Description (Pin diagram) of 8085 Microprocessor
	17.03.22	1	1.6 Register Organizations, Distinguish between SPR & GPR, Assignment 1 cum Doubt Clearing
3 rd	22.03.22	1	1.6 Timing & Control Module,
	23.03.22	1	1.7 Stack, Stack pointer & Stack top.
	24.03.22	1	1.8 Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM)
	25.03.22	1	Class test 1
	26.03.22	1	2.1 Addressing data & Differentiate between one-byte, two-byte & three -byte instructions with Examples.
4 th	29.03.22	1	2.2 Addressing modes in instructions with suitable examples.
	30.03.22	1	2.3 Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching , Stack& I/O , Machine Control)
	31.03.22	1	2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction
	02.04.22	1	2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits.
		1	Assignment 2 cum Doubt Clearing

BHUBANANANDA ODISHA SCHOOL OF ENGINEERING, CUTTACK
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LESSON PLAN

5 th	05.04.22	1	2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register) 2.4.4 Looping, Counting & Indexing (Call/JMP etc)
	06.04.22	1	2.4.5 Stack & Sub routines programmes.
	07.04.22	1	2.4.6 Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.
	08.04.22	1	2.4.7 Compare between two numbers. 2.4.8 Array Handling (Largest number & smallest number in the array)
	09.04.22	1	Unit-3: TIMING DIAGRAMS. 3.1 Define opcode
6 th	12.04.22	1	3.2 Draw timing diagram for memory read
	13.04.22	1	3.3 Draw a neat sketch for the timing diagram for 8085 instruction (MOV, MVI, LDA instruction).
	16.04.22	1	Class test 2
7 th	19.04.22	1	Unit-4 Microprocessor Based System Development Aids 4.1 Concept of interfacing
	20.04.22	1	4.2 Define Mapping & Data transfer mechanisms – Memory mapping & I/O Mapping
	21.04.22	1	4.3 Concept of Memory Interfacing:- Interfacing EPROM & RAM Memories
	22.04.22	1	4.4 Concept of Address decoding for I/O devices .
8 th	26.04.22	1	4.5 Programmable Peripheral Interface: 8255
	27.04.22	1	4.6 ADC & DAC with Interfacing
	28.04.22	1	4.7 Interfacing Seven Segment Displays
	29.04.22	1	4.8 Generate square waves on all lines of 8255
	30.04.22	1	4.9 Design Interface a traffic light control system using 8255. Assignment 3 cum Doubt Clearing

BHUBANANANDA ODISHA SCHOOL OF ENGINEERING, CUTTACK
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
LESSON PLAN

9 th	04.05.22	1	4.10 Design interface for stepper motor control using 8255.
	05.05.22	1	Unit-5 Microprocessor (Architecture and Programming-16 bit-8086)
	06.05.22	1	5.1 Register Organisation of 8086
	07.05.22	1	5.2 Internal architecture of 8086
			5.3 Signal Description of 8086
10 th	10.05.22	1	5.4 General Bus Operation & Physical Memory Organisation
	11.05.22	1	INTERNAL TEST
	12.05.22	1	INTERNAL TEST
	13.05.22	1	5.5 Minimum Mode & Timings 5.6 Maximum Mode & Timings
	14.05.22	1	5.7 Interrupts and Interrupt Service Routines, Interrupt Cycle, Non-Maskable Interrupt, Maskable Interrupt
11 th	17.05.22	1	5.8 8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler Directives and Operators,
	18.05.22	1	5.9 Simple Assembly language programming using 8086 instructions
	19.05.22	1	5.7 Interrupts and Interrupt Service Routines, Interrupt Cycle, Non-Maskable Interrupt, Maskable Interrupt
	20.05.22	1	Unit-6 Microcontroller (Architecture and Programming-8 bit)
	21.05.22	1	6.1 Distinguish between Microprocessor & Microcontroller 6.2 8 bit & 16 bit microcontroller 6.3 CISC & RISC processor
12 th	24.05.22	1	6.4 Architecture of 8051 Microcontroller
	25.05.22	1	6.5 Signal Description of 8051 Microcontrollers
	26.05.22	1	6.4 Architecture of 8051 Microcontroller
	27.05.22	1	6.5 Signal Description of 8051 Microcontrollers

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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
LESSON PLAN

13 th	31.05.22	1	6.6 Memory Organisation-RAM structure, SFR
	01.06.22	1	6.7 Registers,timers,interrupts of 8051 Microcontrollers 6.8 Addressing Modes of 8051
	02.06.22	1	Class test 3
	03.06.22	1	Assignment 4 cum Doubt Clearing
	04.06.22	1	Revision Chapter 1 & 2
14 th	07.06.22	1	Revision Chapter 4 & 5
	08.06.22	1	Revision Chapter 6
	09.06.22	1	REVISION AND PERVIOUS YEAR QUESTION DISCUSSION
	10.06.22	1	REVISION AND PERVIOUS YEAR QUESTION DISCUSSION